

2007 CALICUT UNIVERSITY
IV SEMESTER B.TECH COMPUTER SCIENCE & ENGINEERING
MICROPROCESSOR BASED DESIGN

JUNE 2007

TIME::3 HOUR
MARK:100

ANSWER ANY TEN QUESTIONS QUESTIONS CARRY EQUAL MARKS

MARKS [10*10=100]

1. What is the purpose of driver controller? Explain their functioning in brief.
2. What is latch? Why buffers often required in 8086/8088 are based systems?
3. List and explain the various data addressing modes.
4. What is a macro? Give an example program for a macro.
5. Describe the working of 74ls 139 decoder.
6. What is baud rate? Define the terms : simplex half duplex and full duplex.
7. What is an interrupt? Write down the sequence of operation of a real mode interrupt.
8. Describe the DMA transfer. Explain the functions of 8237 dma controller even used for DMA transfer.
9. Explain the operations of the bus signals and the basic read and writing timr of 8086/8088.
10. Discuss the features of 80386 processors.
11. What are the three different program memory addressing modes available in 8086 and explain them in detail.
12. Write an assembly language program to find the sum and average of n integer elements.
13. Explain how RAM and ROM are interfaced to a microprocessor in detail.
14. Draw the functional block diagram and working of 8279 keyboard/display controller.
15. Describe the function of trap interrupt flag bit and the operation of trap generated tracing in detail.
16. Describe the features if disk memory systems and their operation and usage in computer system

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