

NAME \_\_\_\_\_

ROLLNO \_\_\_\_\_

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY-2006**

**IV B.TECH II SEMESTER SUPPLEMENTARY EXAMINATIONS  
PRINCIPLE OF SOFTWARE ENGINEERING  
(ELECTRICAL AND ELECTRONIC & ENGINEERING)**

**APRIL/MAY 2006**

**MARK-3 HOUR  
MARK-80**

**ANSWER ANY FIVE QUESTIONS. ALL QUESTIONS CARRY EQUAL MARKS.**

**MARKS [16\*5=80]**

1. Explain the prototyping software process model and write its limitations.
2. (a) Is it possible for spoilage to increase while defects/kLOC decrease? Explain  
(b) Explain project planning objectives.
3. Explain about software configuration management .
4. Explain about Jackson System Development.
5. Develop a complete flow model for the photocopier software.
6. Write about:
  - (a) The design model
  - (b) Design documentation
7. Explain clearly about how transactions analysis is useful in the dataflow oriented design.
8. Write notes on the following:
  - (a) graph matrices
  - (b) testing documentation and help facilities
  - (c) flow graph
  - (d) basis path testing