2007-CALICUT UNIVERSITY

B.TECH IV SEMESTER DEGREE EXAMINATION

COMPUTER ORGANIZATION AND DESIGN

(COMPUTER SCIENCE AND ENGINEERING)

TIME-3HOUR
MARKS-100

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1. What are addressing modes? Explain any five addressing modes and shaow how operands are identified in each addressing mode using a neat diagram.

2. Explain the use of synthetic benchmarks?

3. Draw the flowchart for floating point multiplication and explain.

4. Convert: (254)10=(-)2=(-)16.

5. Explain how exceptions are handled.

6. Write the steps needed for executing a R-type instruction.

7. State the principle of locality. Explain its importance in memory organization.

8. What are the steps that are performed in executing the write system call?

9. Explain the working of a CRT based display device.

10. Write a procedure to calculate the factorial of a number. Shaow how stacks are used in parameter passing.

11. Explain the working of a carry look ahead adder.

12. Design a 4-bit ALU that performs AND, OR and addition on a and b or a and b.

13. What are the drawbacks of a single cycle implementation? Explain the implementation of multiple clock cycle in detail.

14. Draw the complete finite state machine control for the data path and explain.

15. Explain the different methods used for transferring data between a device and memory.

16. When a DMA transfer is done between a disk and the main memory in a computer system that has a main memory cache, there are two ways to proceed : the transfer can go through the cache or it can bypass the cache. Discuss the advantages and disadvantages of each choice.

17. Explain the working of a mouse.