DEPARTMENT OF GOVERNMENT EXAMINATIONS HIGHER SECONDARY FIRST YEAR EXAMINATION - MARCH - 2018 KEY ANSWERS FOR COMPUTER SCIENCE

Note:

1. Answers written only in BLACK or BLUE should be evaluated.

- 2. In Section I Choose the correct answer and write the option code with corresponding answer.
- 3. Mark shall also be awarded either for the correct option code or for the correct corresponding answer alone. (This year only)
- 4. If one of them (option or answer) is wrong, then award zero mark only.

Maximum Marks - 70

31	State Viscatuames a Section - Itagus bris another of 2 viscatuames a				
Ansı	wer al	I the Questions	April Section		15×1=15
1	b	2	9	b	System tray
2	а	Lotus 1-2-3	10	b	Ctrl Company of
3	d	2 ¹⁰	11	b	Display properties
4	d	4:3	12	C	passwd
5	b	2	13	d	& per to a stable on .
6	а	Direct Memory Access	14	С	Auto
7	d	System Software	15	а	get different eller
8	а	3			AND MISSELVE TO THE SET OF

SECTION - II

Ans	wer any Six Questions (Question No.16 is	Compu	Isory)	/ 18.77. Y	6×2=12	TH.
16	A literal is the appearance of a variable or its expression.			Boolean		2 2 1
17	Temporary storage area inside the CPU.They are high-speed memories.	pril 28%	.21 . Piess		g best or	2
	Hold data for immediate processing and record (or) The registers (internal memory) are used the execution of the processor.		50°.01	វីណ្ឌីស្រ្តី ន	data for	* 08
18	ert) ou securi de la riquis britarida Di Torra Input Tolliu (ed Output Tolliu) en Output Tolliu (ed Output Tolliu)					
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19	1. Analog data transmission	1	0		- Biden	1
19	of ters and a vertical method of a such	1	0 1 			2
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	Analog data transmission Digital data transmission	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	0 1 		e december de la composition della composition	8 :
19 20 21	Analog data transmission Digital data transmission Windows XP is an user friendly operating system.	1 1 nequest	0 1 		Tacalcular Tacalc	8 :

23	PERAFIMENT OF COVERNMENT EXAMINATIONS HIGHER SECONDARY FIRST VEAL XAMINATION 2018	
	Start, End Input, Output	0.00
	Calculations Decision	. 2003
	as written only in BLACK or BLUE Orould be evaluated.	WELL I
gnit	TOGSSTICS HIVE S Direction of control now!! SHIVE G connector True Monteo and Second 1 - Holt	195 Al &
24	A style attribute can also be used with some HTML tags to change the characteristics of the tag on which it is applied. If the style is used as an attribute, it will affect only that tag in which it is specified.	ANDREAS

SECTION - III

Anei	wer any Six Questions and Question No.29 is compulsory	×3=18
25	Workstations	19/4/1
	 Personal Computers Laptop Computers Palm PCs 	3 b
26	A maxterm is a sum term in a Boolean expression, which includes all possible variables in true or complement form.	3
27	 registers cache primary memory secondary memory 	3
28	A sequential circuit is a logic circuit who's output are based on, not only the inputs present at that time, but also on the previous state output are called sequential circuits	3
29	In Distributed Operating System the user's job is executed in an idle machine in the network and the result is communicated to the user machine. Distributed Operating System ensures that the entire network behaves as a single computer All the computer become a part of one or other network.	3
30	 First start one application. The application appears on the screen in a window. At the same time, a button with the name of the application appears on the taskbar. Now, start the second application the window of the second application appears on the screen. The button of the second application appears in the taskbar, The title bar of the first application and its button on the task bar become dim, the buttons on the taskbar are used to switch between the different 	3
31	 The Run command on the Start menu offers an alternate method, to start applications or open data files. Start → Run command is used to open run dialog box 	3

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32		Property of the second
	cat command more com	nmand
er F	1. Display the content of the file and if the file is lenthy it shows the end of the page	ontent of the
7	in forward or backward 2. We can move forward or backward	
33	An algorithm is a procedure with the following properties.There should be a finite number of steps.	ਨੇ ਜ਼ਿਲ੍ਹੇ ਹਰ ਜਨ੍ਹਾਂ ਵਿਚ ਹਨ। ਜ਼ਿਲ੍ਹੇ ਗੁਰੂ ਜ਼ਿਲ੍ਹੇ
	Each step is executable without any ambiguity.	
	 Each step is executable within a finite amount of time, u memory space. 	sing a finite amount of
	The entire program should be executed within a finite are	mount of time.

SECTION - IV

Ansv	ver all the questions	5×5=25
34	First Generation - 1940-1956: Vacuum Tubes Second Generation - 1956-1963: Transistors Third Generation - 1964-1971: Integrated Circuits Fourth Generation - 1971-Present: Microprocessors Fifth Generation - Present and Beyond: Artificial Intelligence with explanations (OR) Using 4 bit calculation a. 11001 b. 11111001	(p. 5
35	G. 0100	4 T 3
3	 Keyboard Mouse Scanner Bar code reader Digital camera Touch sensitive screen MICR OCR OMR Light pen Magnetic reader. (Any Five with Explanations) (OR) 	
	The steps involved in designing the logic circuit: Step 1: Statement of the problem given. Step 2: Identify inputs and outputs. Step 3: Formulate truth table. Step 4: Convert the truth table to a Boolean function. Step 5: Realization of the Boolean function into a logic Circuit Diagram. Explanation with suitable example	

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30	Ling Ling Lang	
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	(With Explanations)	1 1
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	Drop-down Isit boxes and provided and also earlies of the Armburghan	No. 05
	Radio buttons	*
	Check boxes yna fuorthwr udatumous a getta costs	t
8	Buttons	4
	Lads	
	Sliders	5
	Sliders (With Explanations)	
37	Star network	America
Chind	Ring network	1 18
	Bus network	M. M.C.
	Hybrid network	
	EDDL potwork ENGLY DESCRIPTION OF THE PROPERTY OF	
	(With Explanations)	
	The third is the sound of the control of the contro	5
	System administrator / root_user	V
	File owner	
	Group owner double old 6 priell political and 4 prie	
	Other usres	5
	(With Explanations)	
38	Relevant 'C' Program	
30	(OR)	35
	(Oh)	4-
	- To Create Table	
	- To Create Table Row	p. 1
	- To insert a new cell - represent the Column Heading	5
	- To represent a entry in the table	
		9-
	Attributes	A Property
* 1	Border, Bgcolor, cell spacing and cell spadding (with explanations)	9
	(Any Relevant Example)	
C	Washing Tubling LIANY HIVE WITH EXPLANATIONS)	4

4

Stap * Pedization of the Budger tunction into a logic Circuit Diagram.

The steps in Advector designing mellogic dirouit:

Step 4. Conventition cate to a Boclean function.

Step 2 - to skifty applis grai pulsats. Step 3 i Entrapleta auto trate.

Ехрівлей о уділ чодабів єхегірій